INTEL INVENTION DISCLOSURE ATTORNEY-CLIENT PRIVILEGED COMMUNICATION located at http://legal.intel.com/patent/index.htm

27946

DATE: 6-14-02

TMG-TM/ICG/OPG/OPD

It is important to provide accurate and detailed information on this form. The information will be used to evaluate your invention for possible filing as a patent application. Invention Disclosure forms MUST be sent electronically via email to your manager/supervisor who should then forward with their approval to our email account "invention disclosure submission." If you have any questions, please call 8-264-0444.

Last Name: Mader	First Name: Thomas		M.I. B
Intel Phone Number: (510) 578-5617	Intel Fax Number: (510) 579-5917		Mailstop: TB2
E-mail address: tom.mader@intel.com		WWID: 10965328	
Citizenship: USA	Are you a contractor?	Yes:	No: X
Home Address: 16251 Azalea Way		,	
City: Los Gatos	State: CA	Zip: 95032	Country: USA
Corporate Level Group: ICG	Division: OPG		Subdivision: OPD
Supervisor: Dan Sweeney	WWID: 10034224	M/S: TB1	Phone #: 510-578-5637
Last Name: Buchheit	First Name: Steve		M.I. J
Intel Phone Number: 510 578-5610	Intel Fax Number: 510 578-5910		
E-mail address: steve.buchheit@intel.com	WWID: 10965253		
Citizenship: USA	Are you a contractor?	Yes:	No: X
Home Address: 389 Waverley St	Ale you a contractor?	res.	I NO: X
City: Menlo Park	State: CA	Zip: 94025	Country: USA
Corporate Level Group: ICG	Division: OPG	Zip. 94025	Subdivision: OPD
Supervisor: Tom Mader	WWID:	M/S: TB2	Phone #: 510 578 5610
- Ton Made	, www.	IW/G. IBZ	Filone #. 510 578 5610
Last Name: Rose	First Name: Dana		M.L.L.
Intel Phone Number: 510-578-5627	Intel Fax Number: 510-578-5921		Mailstop: TB2
E-mail address: Dana.Rose@Intel.com			WWID: 10456448
Citizenship: USA	Are you a contractor?	Yes:	No: X
Home Address: 6307 Hematite Court			
City: San Jose	State: CA	Zip: 95135	Country: USA
Corporate Level Group: ICG	Division: OPG		Subdivision: OPD
Supervisor: Tom Mader	WWID: 10965328	M/S: TB2	Phone #: 510-578-5617
Last Name: KIM	First Name : DAEHWA		
Intel Phone Number: 510 578-5653			M.I. D
E-mail address: DANIEL.D.KIM@INTEL.COM	Intel Fax Number: 510	578-5953	Mailstop: TB2
Citizenship: USA	TA	134	WWID: 10665284
Home Address: 1366 HAMPTON DRIVE	Are you a contractor?	Yes:	No: XXXXXXXXXX
City: SUNNYVALE	State: CA	71 04007	10-1-101
Corporate Level Group: ICG	Division: OPG	Zip: 94087	Country: USA
Supervisor: DANA ROSE	WWID: 10456448	M/S: TB2	Subdivision: OPD
Caparvisor. BAIVA NOOL	VVVVID: 10436446	1W/5: 1BZ	Phone #: 510 578-5627
Last Name: OEN	First Name: JOSHUA		M.I. T
Intel Phone Number: 510-578-5628	Intel Fax Number: 510	578-59-28	Mailstop: TB2
E-mail address: JOSHUA.OEN@INTEL.COM			WWID:10965339
Citizenship: USA	Are you a contractor?	Yes:	No: XXXXXXXXX
Home Address: 894 BARCELONA DRV			
City: FREMONT	State: CA	Zip: 94536	Country: USA
Corporate Level Group: ICG	Division: OPG		Subdivision: OPD
Supervisor: STEVE BUCHHEIT	WWID: 10965253	M/S: TB2	Phone #: 510 578-5610

(PROVIDE SAME INFORMATION AS ABOVE FOR EACH ADDITIONAL INVENTOR)

2. Title of Invention: OPTICAL XPAK TO XENPAK ADAPTER

3. What technology/product/process (code name) does your invention relate to (be specific if you can)

Related product name Xenpak & Xpak

Include several key words to describe the technology area of the invention in addition to # 3 above: Xpak MSA, Xenpak MSA, 10gig transponder,

Stage of development (i.e. % complete, simulations done, test chips if any, etc.): Product currently in conceptual stage

6a. Has a description of your invention been (or planned to be) published outside of Intel: Not at this time

If YES, was the manuscript submitted for pre-publication approval through the Author Incentive Program:

If YES, please identify the publication and the date published:

6b. Has your invention been used/sold or planned to be used/sold by Intel or others? Planned to be released

If YES, date it was sold or will be sold: Alpha samples in November, 2002 time frame

6c. Does this invention relate to technology that is or will be covered by a SIG (special interest group)/standard or specification?

If YES, name of SIG/standard/specification:

6d. If the invention is embodied in a semiconductor device, actual or anticipated date of tapeout? Not applicable

6e. If the Invention is software, actual or anticipated date of any beta tests outside Intel: Not applicable

7. Was the invention conceived or constructed in collaboration with anyone other than an Intel blue badge employee or in performance of a project involving entities other than Intel (e.g. government, other companies, universities or consortia)? NO: NO If YES, name of individual or entity.

Is this invention related to any other invention disclosure that you have recently submitted? If so, please give the title and inventors: NO

PLEASE READ AND FOLLOW THE DIRECTIONS ON HOW TO WRITE A DESCRIPTION OF YOUR INVENTION

Try to limit your description to 2-3 pages
Do NOT attach a presentation, white paper, or specification
ANSWER ALL OF THE QUESTIONS BELOW

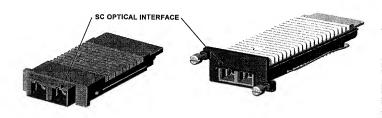
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Please provide a description of the invention and include the following information:

Describe in detail what the components of the Invention are and how the Invention works.
 Currently the Xenpak and Xpak modules are designed independently without any interchangeability of parts. The proposed concept is to make the functionality of the PC board

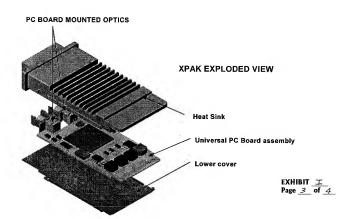
assembly be adaptable to both products and change the mechanical features in order to satisfy both product requirements. The proposed concept will allows a PC board assembly from the Xpak to be used in the Xenpak Transponder. The PC board assembly is designed to fit the Xpak module, but for the Xenpak module the same PC board assembly will create an incorrect position of the Optical interface. The key in allowing the transformation of a Xpak PC board to be used in a Xenpak is an Optical Patchcord. The Optical patchcord is the equivalent of an electrical extension cord and can allow the outside connector interface to be at a different location with respect to the PC board mounted optics.

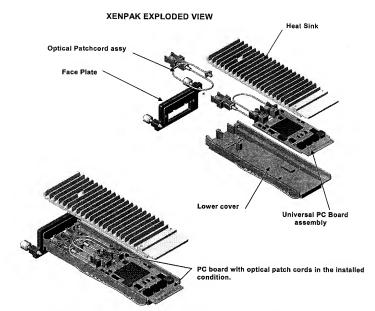
- Describe advantage(s) of your invention over what is currently being done.
 - The advantage of having a common PC board assembly is, reduced part cost, same basic assembly process, testing and qualifying protocol can be consolidated, common programming scheme, product characteristics are similar for customer...etc.
- You MUST include at least one figure illustrating the invention. If the invention relates to software, include a flowchart or pseudo-code representation of the algorithm.



XPAK 10Gbits / Transceiver

XENPAK 10Gbits / Transceiver





- Value of your invention to Intel (how will it be used?). By satisfying the two different markets with one solution (internally) Intel can eliminate most of the development cost of a new product.
- Explain how your invention is novel. If the technology itself is not new, explain what makes it different. Currently the industry solves this type of problem by developing two different solutions for two products. This proposal will allow two different products to share a common core part, the PC board assembly.
- 6. Identify the closest or most pertinent prior art that you are aware of. None that we are aware of.
- Who is likely to want to use this invention or infringe the patent if one is obtained and how would infringement be detected? Our competitors, Picolight, Infineon, JDSU, Agere, Mitsubishi, Hitachi and other transponder manufacturing companies.

sponder manufacturing companies	ibistii, Hitaciii aliu
HAVE YOUR SUPERVISOR READ AND FORWARD IT ELECTRONICALLY	EXHIBIT <u>I</u> Page <u>4</u> of <u>4</u>

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New Client / Matter Fo

Client

11/6/2003 C NEW CLIENT

Page 1

Prepared By: af		Matter	110578-135407]
	CLIENT IN	FORMATION		
Client Model: *				
Client Name 1: *	INTEL LEGAL - PPG/OI	PTICAL-MEMS		
Client Name 2:				
Address Line 1: *	INTEL CORPORATION			-
Address Line 2:	DAVID SIMON		######################################	
Address Line 3:	CHIEF PATENT COUNS	SEL		
Address Line 4:	C/O MALOU DE LEON, SC4-203			
Address Line 5:	2200 MISSION COLLEG	SE BLVD		-
Address Line 6:	SANTA CLARA, CA 950	052		-
City:	SANTA CLARA	Si	tate: CA	
Postal Code:	95052	Country:		
International Prefix No:	Phone I	No: (408) 765-6886	Fax No:	
SS/Tax ID;				
Contact Name:				
Contact Title:				
Contact Email Address:				
Domestic / International:	D	Add To Timekeeper's	Contact List:	1

Notes NEED C/M and file on a RUSH BASIS, PLEASE. Please only create a trifold with side colored barcode stocker and white barcode stocker on it. Check file out to supervising attorney, but deliver to me. Thanks. - Allison Fahl

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